

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Kouta NAGANO et al.

Application No.: 10/519,175

Filed: December 20, 2004

For: Method for Manufacturing  
Semiconductor Device

Confirmation No.: 5251

Examiner: Monica D. Harrison

Art Unit: 2813

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**Amendment**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement mailed January 17, 2007, this Amendment is being filed together with the enclosed Response to Restriction Requirement for the above-referenced application. Please enter the following amendments and remarks:

**Amendments to the Claims** are reflected in the Listing of Claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 4 of this paper.